

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number		09994600
	Filing Date		2001-11-19
	First Named Inventor	Douglas H. Anderson	
	Art Unit	2178	
	Examiner Name	Kyle R. Stork	
	Attorney Docket Number	CD01174M	

**U.S.PATENTS**

Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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9	6611220	B1	2003-08-26	Snyder	Entire Document
10	7180342	B1	2007-02-20	Shutt et al.	Entire Document
11	6765407	B1	2004-07-20	Snyder	Entire Document
12	7386740	B1	2008-06-10	Kutz et al.	Entire Document
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Examiner Initial*	Cite No	Foreign Document Number <sup>3</sup>	Country Code <sup>2i</sup>	Kind Code <sup>4</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	T <sup>5</sup>
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<del>0</del>	<del>USPTO Non-Final Rejection for 10/033,027 (CD00232) dated 12/16/09, 5 pages</del>	<del><input type="checkbox"/></del>
<del>10</del>	<del>USPTO Final Rejection for 10/033,027 (CD00232) dated 06/06/07, 8 pages</del>	<del><input type="checkbox"/></del>
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19	"An Analog PPL-Based Clock and Data Recovery Circuit with High Input Jitter Tolerance;" Sun, Reprinted from IEEE Journal of Solid-State Circuits, 1989; 4 pages	<input type="checkbox"/>
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**EXAMINER SIGNATURE**

Examiner Signature	/Kyle Stork/	Date Considered	08/03/2009
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Doc description: Information Disclosure Statement (IDS) Filed

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	1	PCT/US99/09712	WO	A1	1999-02-25	Schutte	Entire Document	<input type="checkbox"/>
	2	PCT/US96/17305	WO	A1	1996-06-06	Schutte	Entire Document	<input type="checkbox"/>
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	1	<del>USPTO Non-Final Rejection for 00/075,115 (CD00182) dated 10/09/00, 34 pages</del>	<input type="checkbox"/>
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33	Application Number 09/943,062 (CD00228): "Apparatus and Method for Programmable Power Management in a Programmable Analog Circuit Block;" Mar; 46 pages	<input type="checkbox"/>
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STATEMENT BY APPLICANT**  
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Application Number	09994600		
Filing Date	2001-11-19		
First Named Inventor	Douglas H. Anderson		
Art Unit	2178		
Examiner Name	Kyle R. Stork		
Attorney Docket Number	CD01174M		

	37	USPTO Non-Final Rejection for 10/324,455 (CD98087C2) dated 08/21/2003; 4 pages	<input type="checkbox"/>
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Doc code: IDS

Doc description: Information Disclosure Statement (IDS) Filed

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Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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Examiner Initial*	Cite No	Publication Number	Kind Code <sup>1</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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First Named Inventor	Douglas H. Anderson
Art Unit	2178
Examiner Name	Kyle R. Stork
Attorney Docket Number	CD01174M

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	1	"Pod-Wikipedia, the free encyclopedia"; retrieved on November 14, 2005 from <a href="http://en.wikipedia.org/wiki/Pod">http://en.wikipedia.org/wiki/Pod</a> ; 3 pages	<input type="checkbox"/>
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	Filing Date	2001-11-19
	First Named Inventor	Douglas H. Anderson
	Art Unit	2178
	Examiner Name	Kyle R. Stork
	Attorney Docket Number	CD01174M

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	1	"New Object Domain R3 Beta Now Available (Build 134)" March 13, 2001; < <a href="http://web.archive.org/web/200100331202605/www.objectdomain.com/domain30/index.html">http://web.archive.org/web/200100331202605/www.objectdomain.com/domain30/index.html</a> >; 2 pages	<input type="checkbox"/>
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Doc description: Information Disclosure Statement (IDS) Filed

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number	09994600
	Filing Date	2001-11-19
	First Named Inventor	Douglas H. Anderson
	Art Unit	2178
	Examiner Name	Kyle R. Stork
	Attorney Docket Number	CD01174M

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Examiner Initial*	Cite No	Patent Number	Kind Code†	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>5</sup>
	1	<del>USPTO Notice of Allowance for Application Number 09/930,021 (CD00174) dated 11/20/2004; 4 pages</del>	<input type="checkbox"/>
	2	<del>USPTO Final Rejection for Application Number 09/930,021 (CD00174) dated 08/31/2004; 8 pages</del>	<input type="checkbox"/>
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	1	<del>USPTO Notice of Allowance for Application Number 09/875,599 (CD00180) dated 10/17/2005, 4 pages</del>	<input type="checkbox"/>
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**INFORMATION DISCLOSURE  
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Application Number	09994600
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First Named Inventor	Douglas H. Anderson
Art Unit	2178
Examiner Name	Kyle R. Stork
Attorney Docket Number	CD01174M

<del>8</del>	<del>USPTO Final Rejection for Application Number 09/875,500 (CD00180) dated 08/25/2004; 17 pages</del>	<del><input type="checkbox"/></del>
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U.S.PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code†	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	4608502	B1	1986-08-26	Dijkmans et al.	Entire Document
	2	6373954	B1	2002-04-16	Malcolm et al.	Entire Document
	3	5900780	B1	1999-05-04	Hirose et al.	Entire Document
	4	6850117	B1	2005-02-01	Weber et al.	Entire Document
	5	5973368	B1	1999-10-26	Pearce et al.	Entire Document
	6	5248843	B1	1993-09-28	Billings	Entire Document
	7	6079985	B1	2000-06-27	Wohl et al.	Entire Document
	8	5889723	B1	1999-03-30	Pascucci	Entire Document

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9	5564010	B1	1996-10-08	Henry et al.	Entire Document
10	5552725	B1	1996-09-03	Ray et al.	Entire Document
11	5455731	B1	1995-10-03	Parkinson	Entire Document
12	4970408	B1	1990-11-13	Hanke et al.	Entire Document
13	5663900	B1	1997-09-02	Bhandari et al.	Entire Document
14	6057705	B1	2000-05-02	Wojewoda et al.	Entire Document
15	6704879	B1	2004-03-09	Parrish	Entire Document
16	6141764	B1	2000-10-31	Ezell	Entire Document
17	5923284	B1	1999-07-13	Lavelle et al.	Entire Document
18	5850156	B1	1998-12-15	Wittman	Entire Document
19	5777399	B1	1998-07-07	Shibuya	Entire Document

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20	5734272	B1	1998-03-31	Belot et al.	Entire Document
21	4999519	B1	1991-03-12	Kitsukawa et al.	Entire Document
22	6031365	B1	2000-02-29	Sharpe-Geisler	Entire Document
23	6452437	B1	2002-09-17	Takeuchi et al.	Entire Document
24	5430395	B1	1995-07-04	Ichimaru	Entire Document
25	6167559	B1	2000-12-26	Furtek et al.	Entire Document
26	5677691	B1	1997-10-14	Hosticka et al.	Entire Document
27	5790957	B1	1998-08-04	Heidari	Entire Document
28	6614260	B1	2003-09-02	Welch et al.	Entire Document
29	5734334	B1	1998-03-31	Hsieh et al.	Entire Document
30	6509758	B1	2003-01-21	Piasecki et al.	Entire Document

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31	6507215	B1	2003-01-14	Piasecki et al.	Entire Document
32	6356958	B1	2002-03-12	Lin	Entire Document
33	5555452	B1	1996-09-10	Callaway et al.	Entire Document
34	5305312	B1	1994-04-19	Fornek et al.	Entire Document
35	5724009	B1	1998-03-03	Collins et al.	Entire Document
36	6112264	B1	2000-08-29	Beasley et al.	Entire Document
37	6337579	B1	2002-01-08	Mochida	Entire Document
38	6738858	B1	2004-05-18	Fernald et al.	Entire Document
39	5727170	B1	1998-03-10	Mitchell et al.	Entire Document
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42	RE37195	B1	2001-05-29	Kean	Entire Document
43	6246258	B1	2001-06-12	Lesea	Entire Document
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	1	20020121679	A1	2002-10-29	Bazarjani et al.	Entire Document
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	4	Hong et al., "Hierarchical System Test by an IEEE 1149.5 MTM-Bus Slave-Module Interface Core," IEEE, 2000; 14 pages	<input type="checkbox"/>
	5	Haberl et al., "Self Testable Boards with Standard IEEE 1149.5 Module Test and Maintenance (MTM) Bus Interface," IEEE, 1994; 6 pages	<input type="checkbox"/>
	6	Varma et al., "A Structured Test Re-Use Methodology for Core-Based System Chips," IEEE, 1998; 9 pages	<input type="checkbox"/>
	7	Andrews, "Roadmap for Extending IEEE 1149.1 for Hierarchical Control of Locally-Stored, Standardized command Set, Test Programs," IEEE, 1994; 7 pages	<input type="checkbox"/>

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<del>8</del>	<del>Adham et al., "Preliminary Outline of the IEEE P1500 Scalable Architecture for Testing Embedded Cores," IEEE; 6 pages</del>	<del><input type="checkbox"/></del>
9	Ghosh et al., "A Low Overhead Design for Testability and Test Generation Technique for Core-based Systems," IEEE, 1997; 10 pages	<input type="checkbox"/>
10	Zorian, "Test Requirements for Embedded Core-based Systems and IEEE P1500," IEEE, 1997; 9 pages	<input type="checkbox"/>
11	Zorian et al., "Testing Embedded-Core Based System Chips," IEEE, 1998; 14 pages	<input type="checkbox"/>
12	Papachristou et al., "Microprocessor Based Testing for Core-Based System on a Chip," IEEE, 1999; 6 pages	<input type="checkbox"/>
13	Maroufi et al., "Solving the I/O Bandwidth Problem in System on a Chip Testing," IEEE, 2000; 6 pages	<input type="checkbox"/>
14	Marsh, "Smart Tools Illuminate Deeply Embedded Systems," EDN, 2000; 7 pages	<input type="checkbox"/>
15	York et al., "On-chip Support Needed for SOC Debug," Electronic Engineering Times, 1999; 2 pages	<input type="checkbox"/>
16	Atmel Corporation: AT90SC Summary: "Secure Microcontrollers for Smart Cards," 1999; 7 pages	<input type="checkbox"/>
17	Hwang et al., "Integrated circuit for automatically varying resistance in computer system, has pair of transistors connected in parallel with respective resistors such that resistors are bypassed when corresponding transistors are enabled," Derwent Information LTD; 2002; 2 pages	<input type="checkbox"/>
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19	Charles, Jr. et al., "Wirebonding: Reinventing the Process for MCMs," April 1998; IEEE 7th International Conference on Multichip Modules and High Density Packaging; 3 pages	<input type="checkbox"/>
20	Tran et al., "Fine Pitch and Wirebonding and Reliability of Aluminum Capped Copper Bond Pads," May 2000, IEEE Electronic Components and Technology Conference; 8 pages	<input type="checkbox"/>
21	Song et al., "A 50% Power Reduction Scheme for CMOS Relaxation Oscillator," IEEE, 1999; 4 pages	<input type="checkbox"/>
22	"Electronic Circuit Protector-Circuit Breaker," IBM Technical Disclosure Bulletin; Volume 36, Issue 8, August 1, 1993; 1 page	<input type="checkbox"/>
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39	Application Number 09/721,316 (CD98087C1): "Programmable Oscillator Scheme;" Mar et al.; 26 pages	<input type="checkbox"/>
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41	Application Number 09/998,859 (CD01209M): "A System and a Method for Checking Lock Step Consistency between in Circuit Emulation and a Microcontroller while Debugging Process is in Progress;" Nemecek; 33 pages	<input type="checkbox"/>
42	Application Number 09/998,834 (CD01221M): "A System and a Method for Communication between and Ice and a Production Microcontroller while in a Halt State;" Nemecek; 33 pages	<input type="checkbox"/>
43	Application Number 10/113,065 (CD01215M): "System and Method for Automatically Matching Components in a Debugging System;" Nemecek et al.; 32 pages	<input type="checkbox"/>
44	Application Number 09/989,574 (CD01164M): "Method and System for using a Graphics user Interface for Programming an Electronic Device;" Bartz et al.; 43 pages	<input type="checkbox"/>
45	Application Number 09/989,816 (CD01165M): "Datasheet Browsing and Creation with Data-Driven Datasheet Tabs within a Microcontroller Design Tool;" Bartz et al.; 55 pages	<input type="checkbox"/>

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number	09994600
	Filing Date	2001-11-19
	First Named Inventor	Douglas H. Anderson
	Art Unit	2178
	Examiner Name	Kyle R. Stork
	Attorney Docket Number	CD01174M

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	3	USPTO Non-Final Rejection for Application Number 11/644,100 (CD05204) dated 03/09/2009; 11 pages	<input type="checkbox"/>
	4	USPTO Advisory Action for Application Number 11/644,100 (CD05204) dated 02/09/2009; 3 pages	<input type="checkbox"/>
	5	USPTO Final Rejection for Application Number 11/644,100 (CD05204) dated 11/18/2008; 12 pages	<input type="checkbox"/>

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<del>6</del>	<del>USPTO Non-Final Rejection for Application Number 11/644,100 (CD05204) dated 04/14/2008; 10 pages</del>	<del><input type="checkbox"/></del>
<del>7</del>	<del>USPTO Notice of Allowance for Application Number 11/415,588 (CD05028) dated 03/11/2008; 6 pages</del>	<del><input type="checkbox"/></del>
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<del>9</del>	<del>USPTO Final Rejection for Application Number 11/415,588 (CD05028) dated 10/10/2007; 8 pages</del>	<del><input type="checkbox"/></del>
<del>10</del>	<del>USPTO Non-Final Rejection for Application Number 11/415,588 (CD05028) dated 06/13/2007; 8 pages</del>	<del><input type="checkbox"/></del>
<del>11</del>	<del>USPTO Notice of Allowance for Application Number 12/218,404 (CD05028C1) dated 09/19/2009; 7 pages</del>	<del><input type="checkbox"/></del>
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Filing Date	2001-11-19
First Named Inventor	Douglas H. Anderson
Art Unit	2178
Examiner Name	Kyle R. Stork
Attorney Docket Number	CD01174M

1	20050280453	A1	2005-12-22	Hsieh	Entire Document
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Examiner Initial*	Cite No	Foreign Document Number <sup>3</sup>	Country Code <sup>2</sup>	Kind Code <sup>4</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	T <sup>5</sup>
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	1	Application Number 10/226,911 (CD02112): "Calibration of Integrated Circuit Time Constants," Gehring et al.; 32 pages	<input type="checkbox"/>
	2	<del>USPTO Notice of Allowance for Application Number 09/943,149 (CD01085) dated 04/12/2004; 7 pages</del>	<input type="checkbox"/>
	3	<del>USPTO Non-Final Rejection for Application Number 09/943,149 (CD01085) dated 08/28/2003; 9 pages</del>	<input type="checkbox"/>
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6	Application Number 09/943,149 (CD01085): "Method for Phase Locking in a Phase Lock Loop," Moyal et al.; 21 pages	<input type="checkbox"/>
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9	Durham et al., "High-Linearity Conitnuous-Time Filter in 5-V VLSI CMOS," IEEE, 1992; 8 pages	<input type="checkbox"/>
10	Application Number 09/047,595 (CD97115): "Roving Range Control to Limit Receive PLL Frequency of Operation," Scott; 35 pages	<input type="checkbox"/>
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17	Application Number 09/608,753 (CD00018): "PLL Lockout Watchdog," Wilson et al.; 24 pages	<input type="checkbox"/>
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19	Application Number 09/747,262 (CD00085): "Linearized Digital Phase-Locked Loop," Williams et al.; 9 pages	<input type="checkbox"/>
20	Application Number 09/981,448 (CD01102): "Oscillator Tuning Method," Hauck; 28 pages	<input type="checkbox"/>
21	Application Number 09/538,989 (CD98104): "Memory Based Phase Locked Loop," Krishnan; 27 pages	<input type="checkbox"/>
22	Application Number 09/048,905 (CD95045): "Programmable Clock Generator," Mann et al.; 42 pages	<input type="checkbox"/>
23	Application Number 08/865,342 (CD95045C): "Programmable Clock Generator," Mann et al.; 41 pages	<input type="checkbox"/>
24	<del>USPTO Notice of Allowance for Application Number 10/293,392 (CD02135) dated 03/16/2004; 6 pages</del>	<input type="checkbox"/>
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<del>28</del>	<del>USPTO Final Rejection for Application Number 10/288,003 (CD02108) dated 10/06/2004; 6 pages</del>	<del><input type="checkbox"/></del>
<del>29</del>	<del>USPTO Non-Final Rejection for Application Number 10/288,003 (CD02108) dated 04/07/2004; 9 pages</del>	<del><input type="checkbox"/></del>
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Doc description: Information Disclosure Statement (IDS) Filed

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	2	5867046	B1	1999-02-02	Sugasawa, Yasuo	Entire Document
	3	5517198	B1	1996-05-14	McEwan, Thomas E.	Entire Document
	4	5361290	B1	1994-11-01	Akiyama, Shin-ichiro	Entire Document
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10	6292028	B1	2001-09-18	Tomita	Entire Document
11	6559685	B1	2003-05-06	Green	Entire Document
12	6781456	B1	2004-08-24	Pradhan	Entire Document
13	6111431	B1	2000-08-29	Estrada	Entire Document
14	6590422	B1	2003-07-08	Dillon	Entire Document
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20	4727541	B1	1988-02-23	Mori et al.	Entire Document
21	4809345	B1	1989-02-28	Tabata et al.	Entire Document
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42	5043674	B1	1991-08-27	Bonaccio et al.	Entire Document
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49	6294932	B1	2001-09-25	Watarai, Seiichi	Entire Document
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Examiner Initial*	Cite No	Publication Number	Kind Code <sup>1</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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1	20030135842	A1	2003-07-17	Frey et al.	Entire Document
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6	<del>USPTO Non-Final Rejection for Application Number 11/201,922 (CD04111) dated 10/21/2008; 12 pages</del>	<input type="checkbox"/>
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20	Application Number 10/109,979 (CD01211M): "Graphical user interface with logic unifying functions," Anderson et al.; 100 pages	<input type="checkbox"/>
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Examiner Name	Kylie R. Stork
Attorney Docket Number	CD01174M

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number	09994600
	Filing Date	2001-11-19
	First Named Inventor	Douglas H. Anderson
	Art Unit	2178
	Examiner Name	Kyle R. Stork
	Attorney Docket Number	CD01174M

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